

MOTOROLA
SEMICONDUCTOR TECHNICAL DATA

8-Bit Serial-Input/Serial or Parallel-Output Shift Register with Latched 3-State Outputs
High-Performance Silicon-Gate CMOS

The MC5474HC595A is identical in pinout to the LS595. The device meets all requirements with standard CMOS outputs, with signal inverters. They are compatible with LS TTL outputs.

The 74HC595 consists of an 8-bit shift register and an 8-bit D-type latch with three-state parallel outputs. The shift register accepts serial data and provides a synchronous clock and register also provides parallel data to the 8-bit latch. The 8-bit register and latch have independent clock inputs. The device also has an enable/disable input for the shift register.

The 74HC595 device interfaces with the Motorola 8080 and data port on CMOS MPUs and MCUs.

- Output Drive Capability: 15 LS TTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0 μ A
- High Noise Immunity Characteristics of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 328 FETs or 62 Equivalent Gates
- Improved Propagation Delay
 - 50% Lower Quiescent Power
 - Improved Input Noise and Latchup Immunity

LOGIC DIAGRAM

MC5474HC595A

8-BIT SHIFT REGISTER
CMOS 74HC595A

8-BIT LATCH
CMOS 74HC595A

8-BIT 3-STATE OUTPUT
CMOS 74HC595A

ORDERING INFORMATION

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|--------------|------|
| MC5474HC595A | CMOS |
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PIN ASSIGNMENT

| | |
|----|----|
| Q0 | Q7 |
| Q1 | Q6 |
| Q2 | Q5 |
| Q3 | Q4 |
| Q4 | Q3 |
| Q5 | Q2 |
| Q6 | Q1 |
| Q7 | Q0 |

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